

DESCRIPTIVE ABSTRACTA RECEIVER FOR A CDMA SYSTEM

The receiver includes at least one channel (V_1) with an adapted filter (20) and with a symbol clock signal (H_s) recovery circuit (12) and other channels (V_2, \dots, V_k) with a sliding correlator ($30_2, \dots, 30_k$) using the symbol clock signal (H_s) produced by the channel (V_1) with adapted filter.

Application for radiocommunications with mobiles.

Fig. 10

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